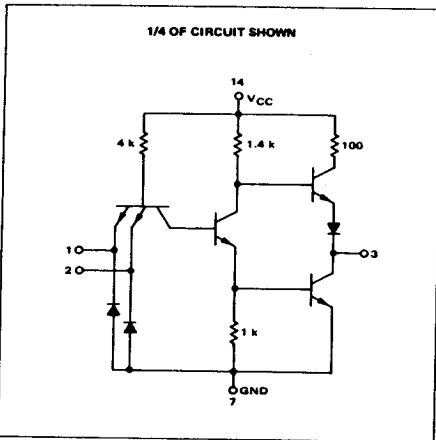


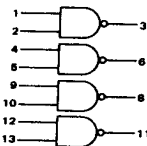
QUAD 2-INPUT "NAND" GATE

MTTL MC7400P series
MTTL MC5400L/7400L series

MC5400L*
MC7400P,L*



This device consists of four 2-input NAND gates. Each gate may be used as an inverter, or two gates may be cross-coupled to form bistable circuits.



Positive Logic: $3 = \overline{1 \times 2}$
Negative Logic: $3 = \overline{1 + 2}$

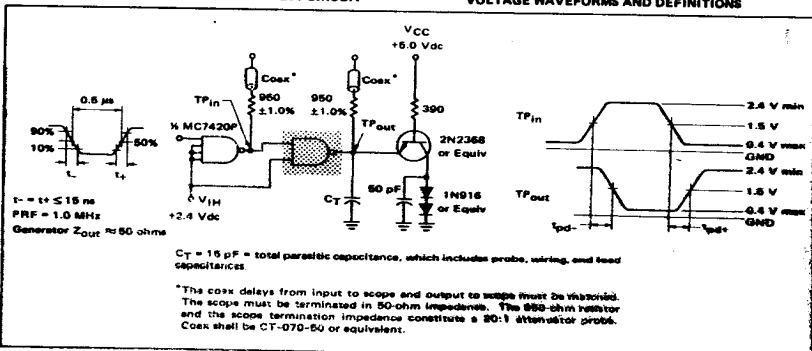
Input Loading Factor = 1
Output Loading Factor = 10

Total Power Dissipation = 40 mW typ/pkg
Propagation Delay Time = 13 ns typ

*L suffix = TO-116 ceramic package (Case 632)
P suffix = TO-116 plastic package (Case 608)
See General Information section for package outline dimensions.

SWITCHING TIME TEST CIRCUIT

VOLTAGE WAVEFORMS AND DEFINITIONS



365

